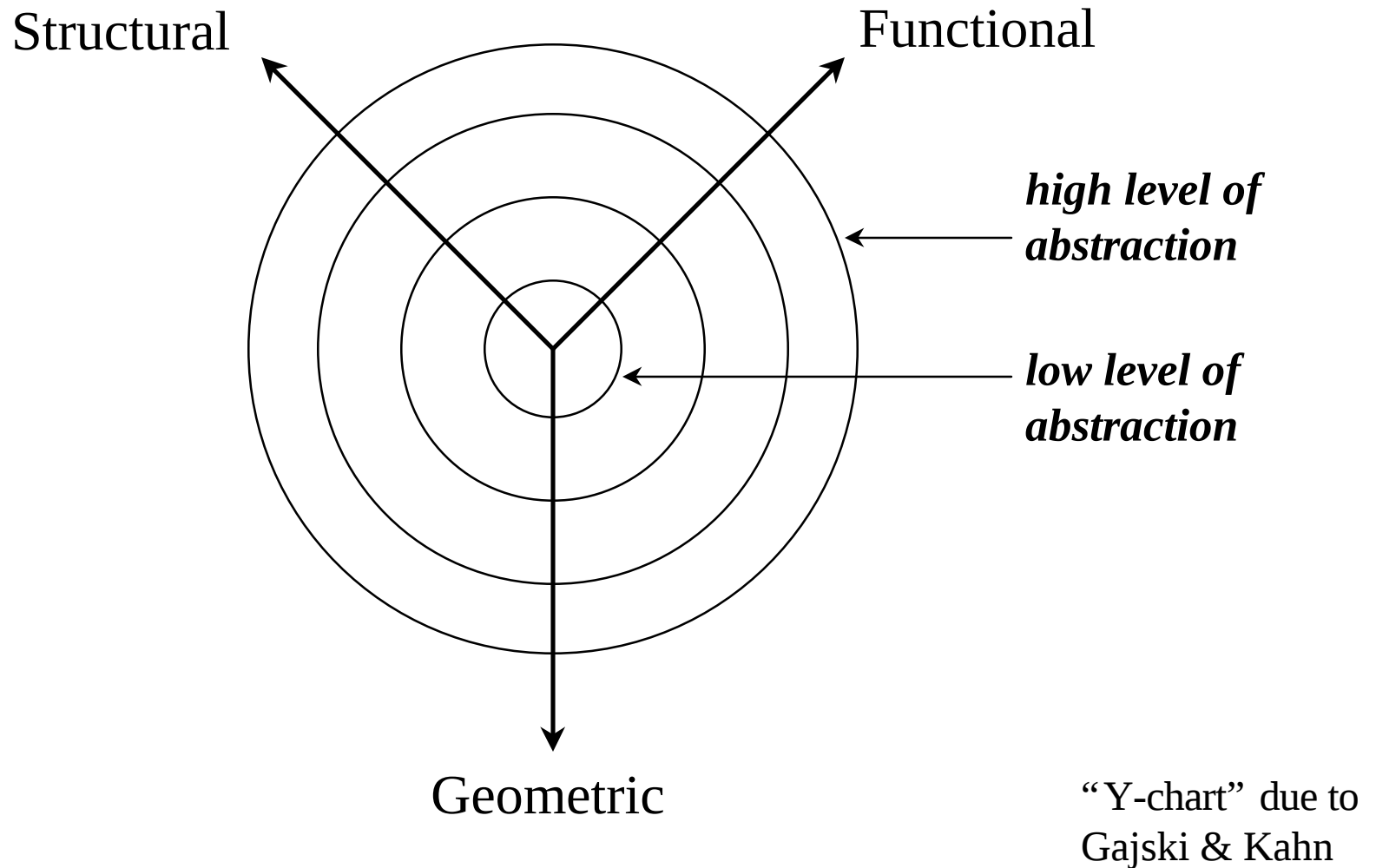
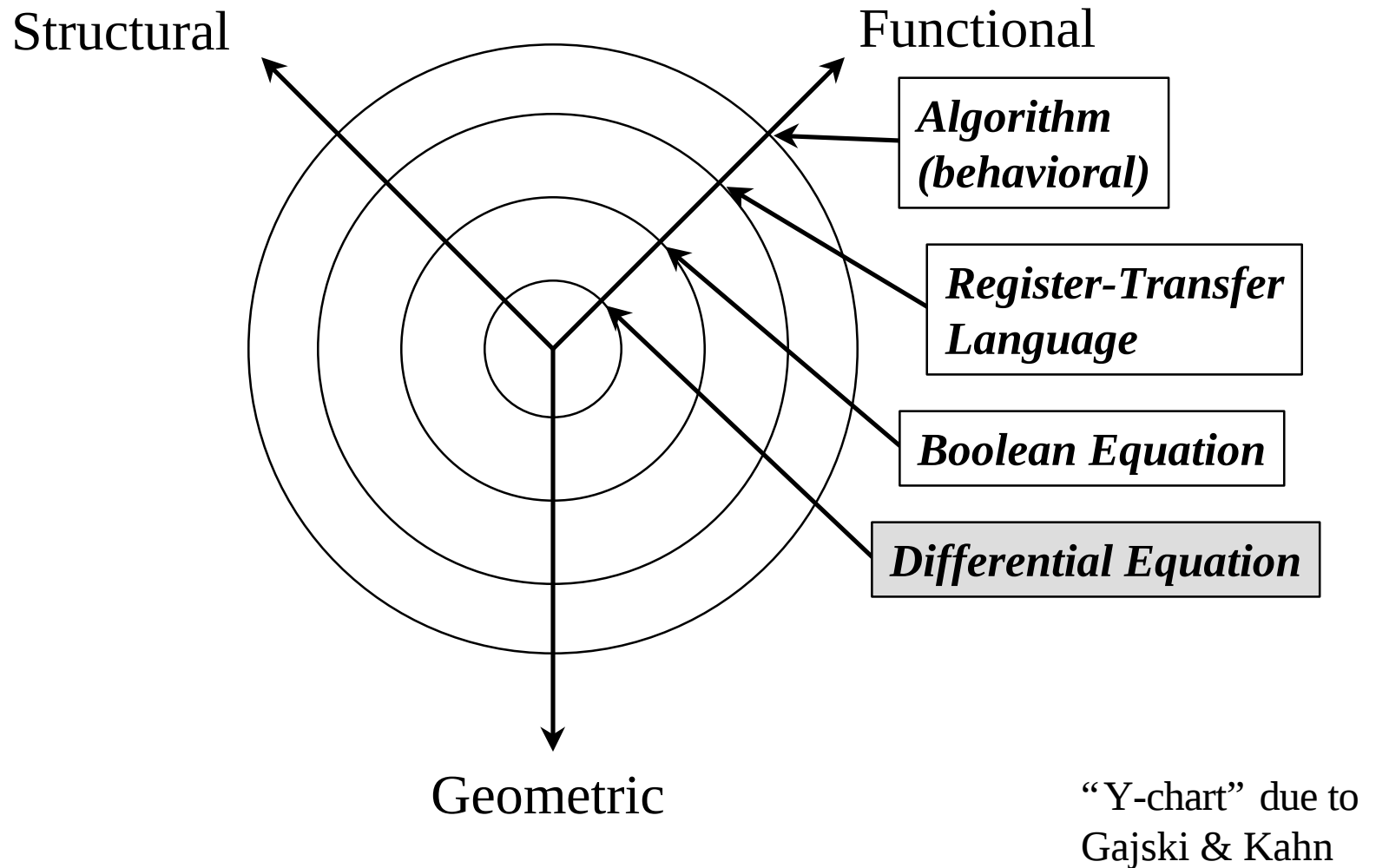


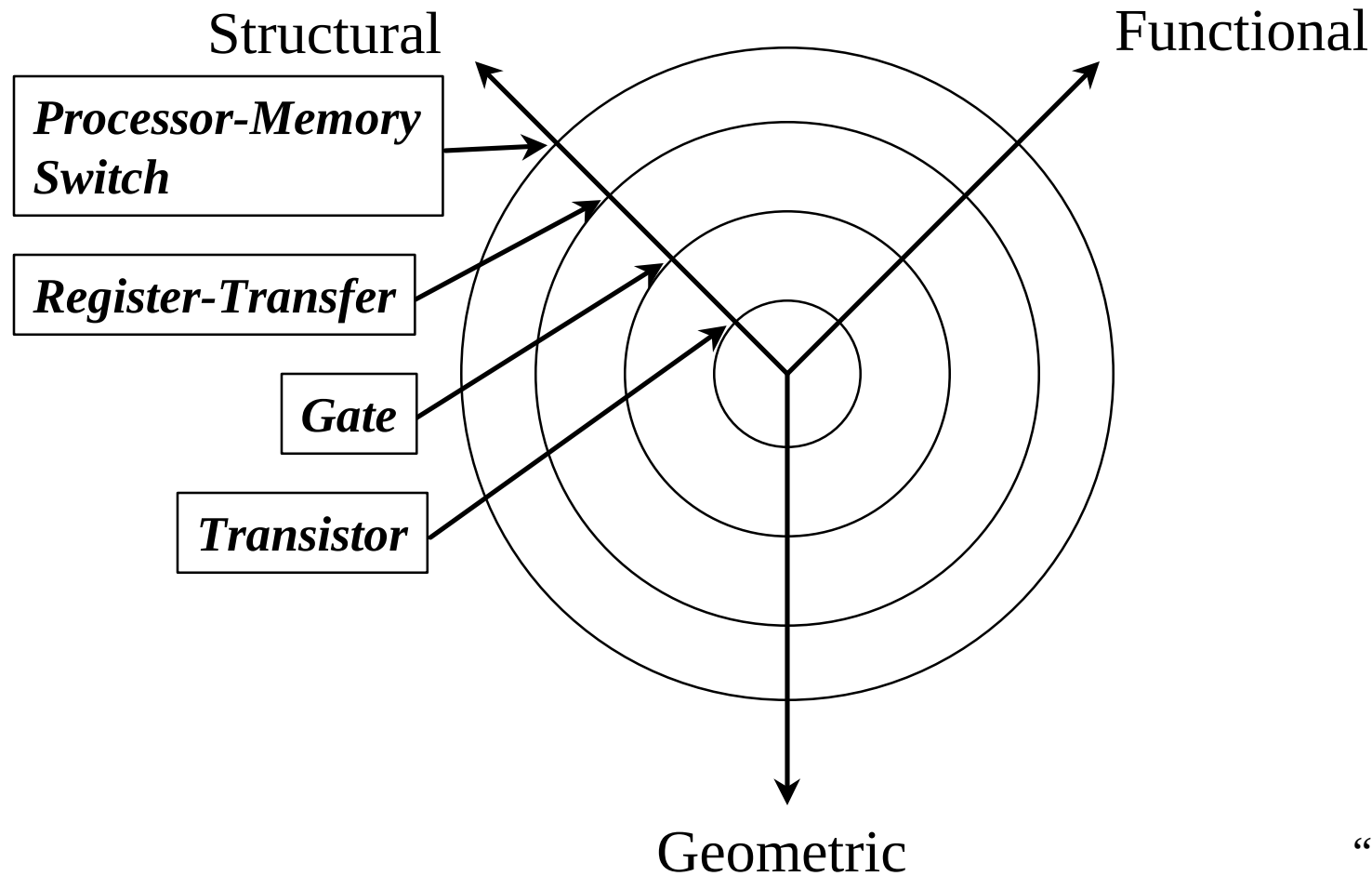
Domains and Levels of Modeling



Domains and Levels of Modeling

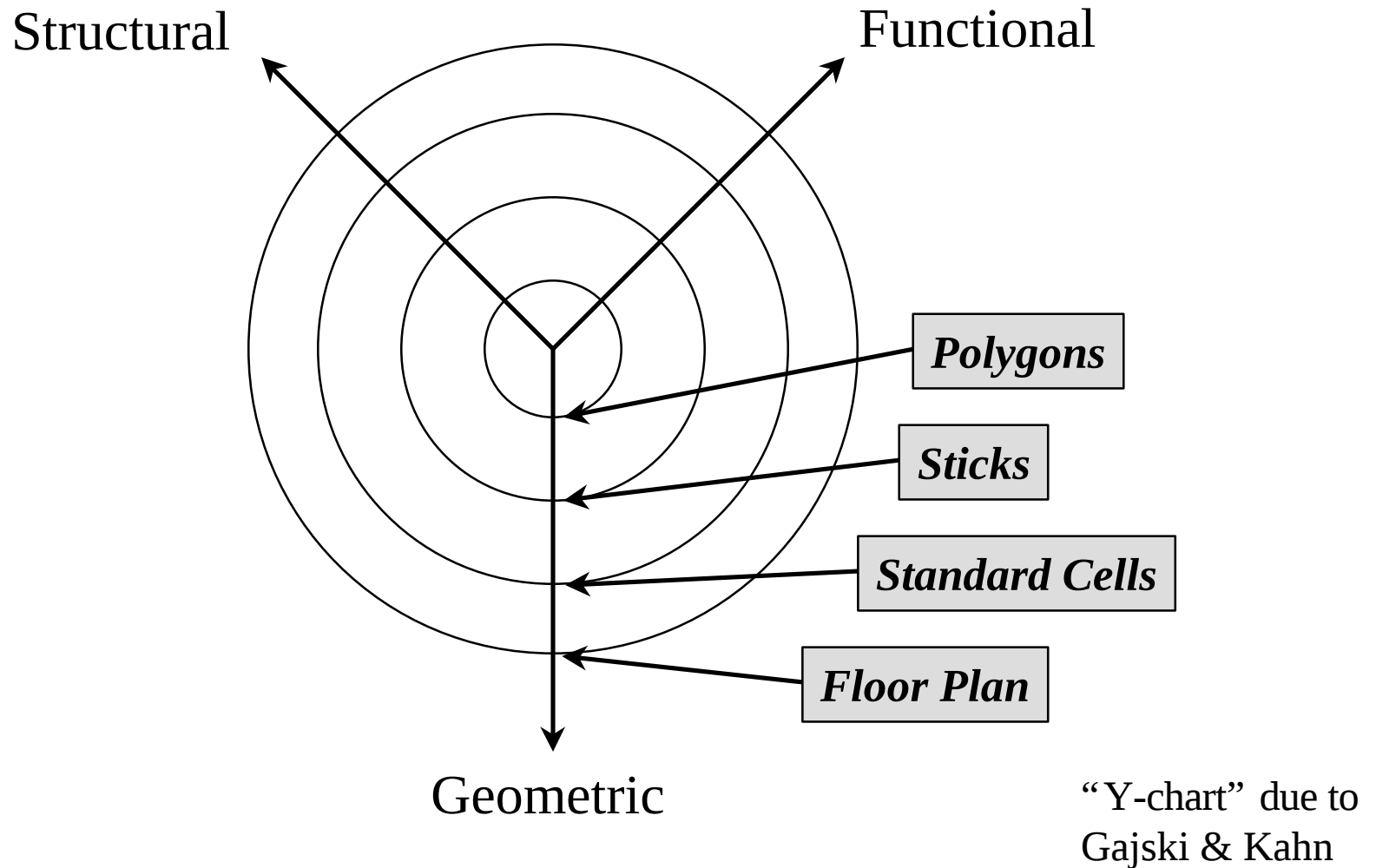


Domains and Levels of Modeling

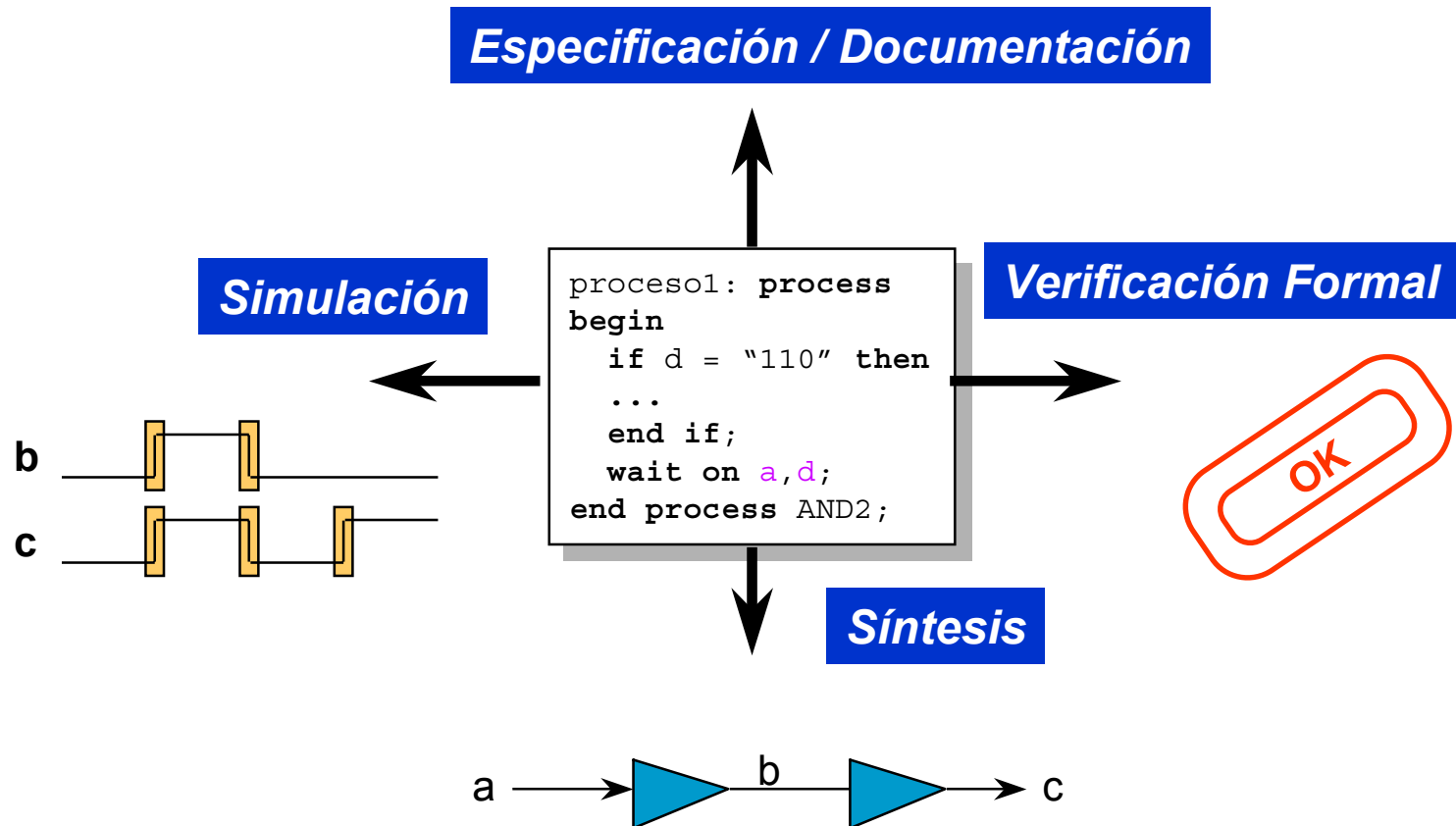


“Y-chart” due to
Gajski & Kahn

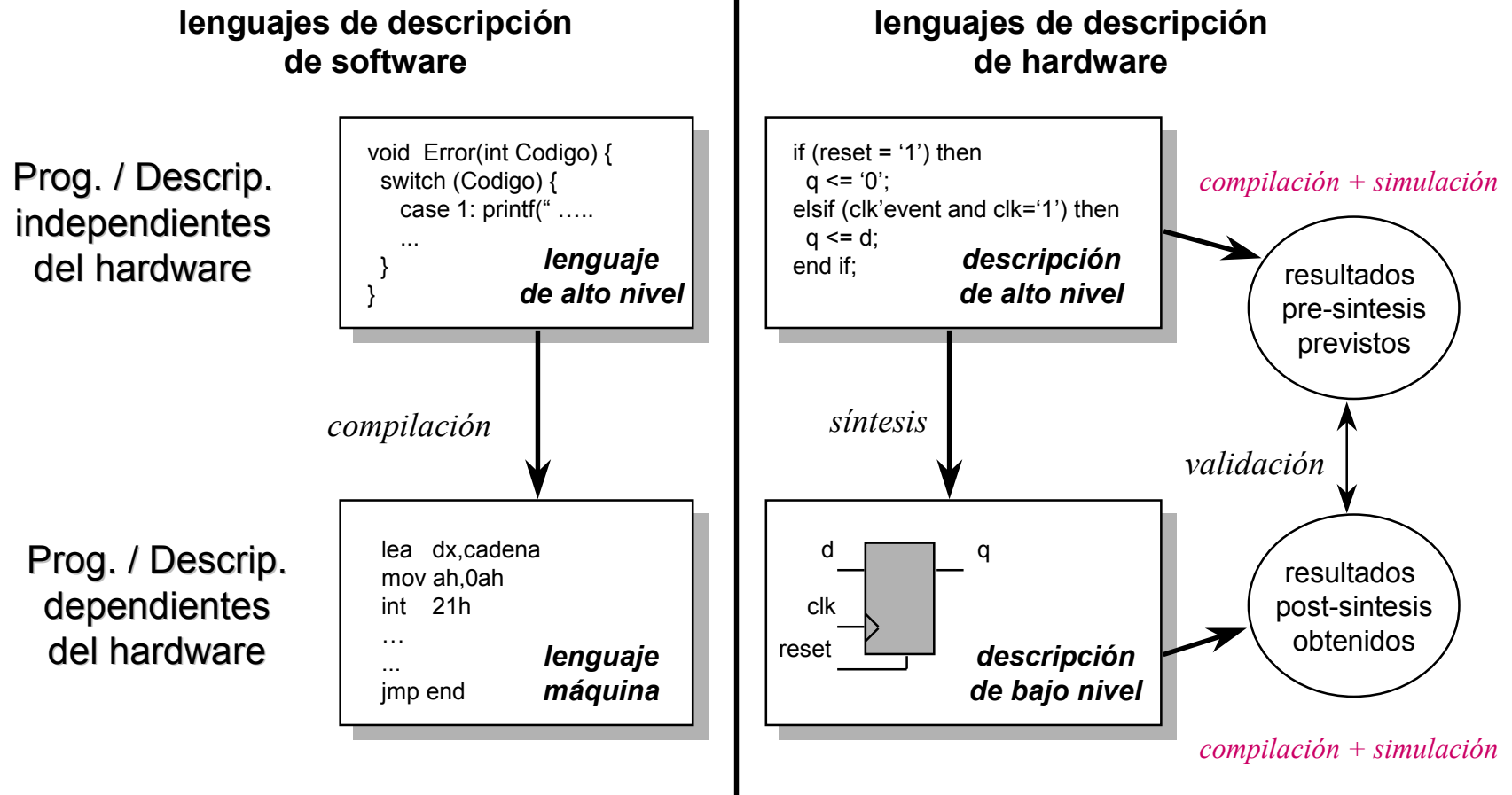
Domains and Levels of Modeling



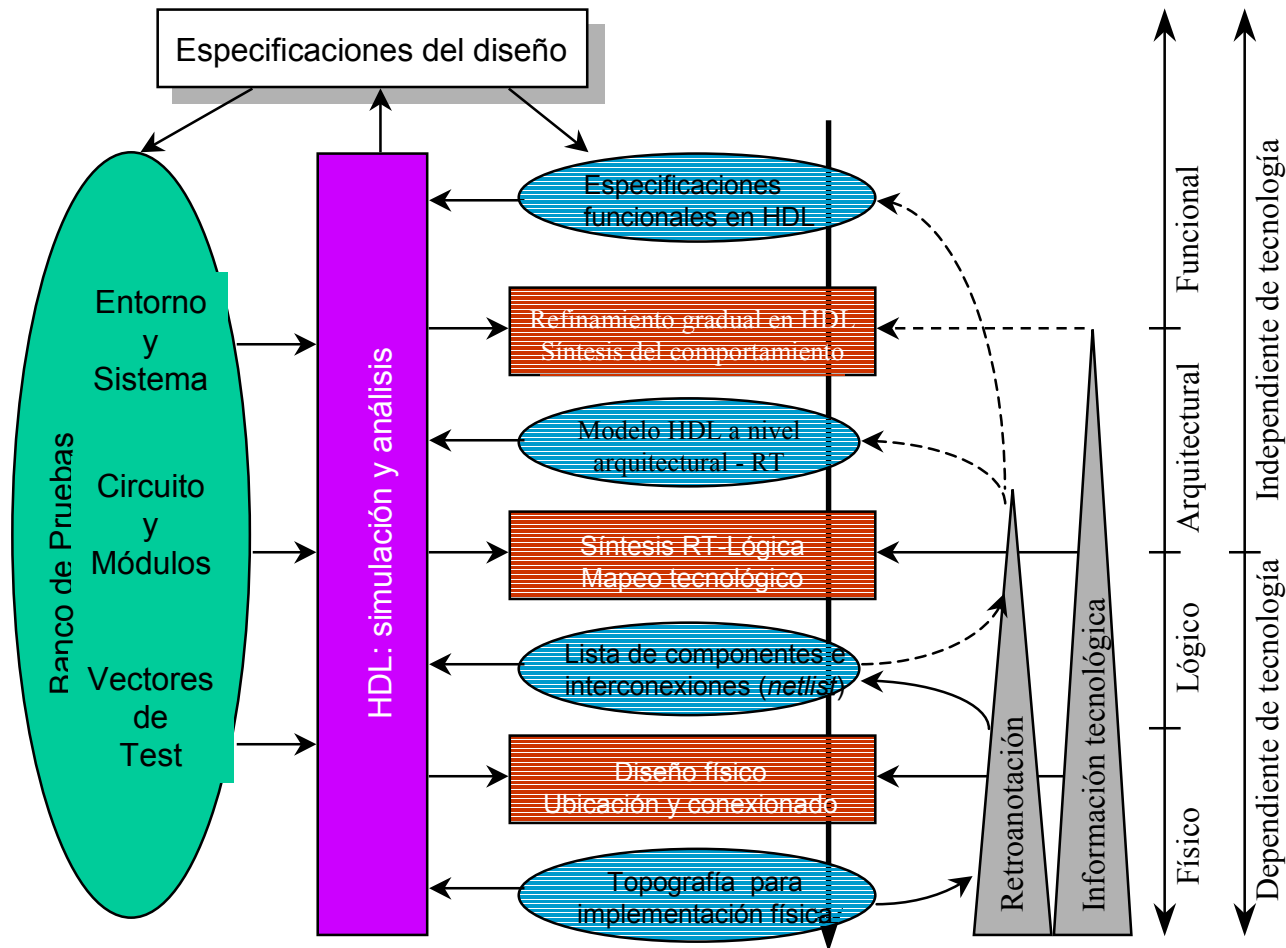
Usos de los HDL



Hardware versus Software

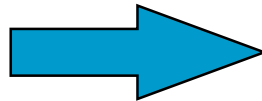
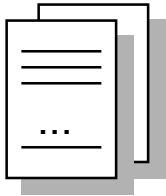


Metodología top-down



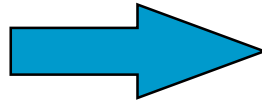
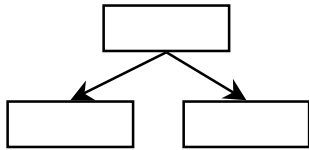
Herramientas CAD/CAE

Nivel Funcional



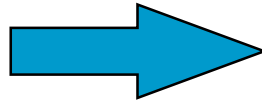
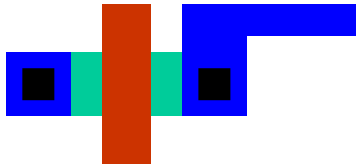
captura gráfica/mixta
simulación HDL
síntesis de alto nivel
verificación formal
testabilidad

Nivel Estructural



captura gráfica/mixta
simulación HDL/lógica
síntesis RTL
generación de estructuras de test
análisis temporal

Nivel Físico



floor planning
place & route
extracción de parámetros
simulación eléctrica
encapsulado